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Set
        Items
                Description
S1
      5847323
                CPU OR CENTRAL() PROCESSING() UNIT? ? OR PROCESSOR? ? OR MIC-
             ROPROCESSOR? ? OR COPROCESSOR? ? OR COMPUTER? ?
S2
                S1(3N)(FIRST OR 1ST OR ORIGINAL OR MAIN OR INITIAL OR PRIME
        69166
              OR PRIMARY OR ONE)
S3
        77857
                S1(3N)(SECOND OR 2ND OR SUB OR BACKUP OR SECONDARY OR ANOT-
             HER OR NEXT OR TWO OR PAIR OR COUPLE OR DUAL OR TWIN) OR SUBC-
             PU OR SUBPROCESSOR? ?
S4
     23285208
                SIGNAL? ? OR DATA OR ANSWER? ? OR OUTPUT OR RESULT? ?
S5
       296979
                S4(3N)(EXPECTED OR STORED OR PRESTORED OR STORAGE OR REFER-
             ENCE)
S6
         7857
                S5(3N)(COMPAR??? OR COMPARISON? ? OR MATCH??? OR MISMATCH?-
             ??)
S7
          815
                S4 (3N) DUMMY
S8
        15496
                SIGNAL? ?(3N) (SIMPLE OR SIMPLIFY??? OR SIMPLIFIE? ? OR MOD-
             IFY??? OR MODIFIED OR MODIFICATION? ? OR STRIPP???)
S9
            Ω
                S2 AND S3 AND S6 AND S7
S10
                S2 AND S3 AND S6:S7
            3
S11
            3
                RD (unique items)
S12
          250
                S2 AND S3 AND S5
S13
            8
                S12 AND CONTROL(2N)SIGNAL? ?
            7
S14
                S13 NOT S11
            5
S15
                RD
                    (unique items)
            5
                S15 NOT PY=2004:2006
S16
S17
            6
                CONTROL(2N)SIGNAL? ? AND S7
S18
           54
                CONTROL(2N)SIGNAL? ? AND S6
S19
           45
                RD (unique items)
           8
S20
                S19 AND S1
S21
           14
                S17 OR S20
S22
           10
                RD (unique items)
```

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File 350: Derwent WPIX 1963-2006/UD, UM &UP=200639
         (c) 2006 The Thomson Corp.
        Items
Set
                Description
S1
      1413929
                CPU OR CENTRAL() PROCESSING() UNIT? ? OR PROCESSOR? ? OR MIC-
             ROPROCESSOR? ? OR COPROCESSOR? ? OR COMPUTER? ?
S2
        60319
                S1(3N) (FIRST OR 1ST OR ORIGINAL OR MAIN OR INITIAL OR PRIME
              OR PRIMARY OR ONE)
S3
        43326
                S1(3N)(SECOND OR 2ND OR SUB OR BACKUP OR SECONDARY OR ANOT-
             HER OR NEXT OR TWO OR PAIR OR COUPLE OR DUAL OR TWIN) OR SUBC-
             PU OR SUBPROCESSOR? ?
      5138948
S4
                SIGNAL? ? OR DATA OR ANSWER? ? OR OUTPUT OR RESULT? ?
       437952
                S4(3N) (EXPECTED OR STORED OR PRESTORED OR STORAGE OR REFER-
S5
             ENCE)
S 6
        29627
                S5(3N)(COMPAR??? OR COMPARISON? ? OR MATCH??? OR MISMATCH?-
             ??)
         5325
S7
                S4 (3N) DUMMY
S8
        16260
                SIGNAL? ?(3N) (SIMPLE OR SIMPLIFY??? OR SIMPLIFIE? ? OR MOD-
             IFY??? OR MODIFIED OR MODIFICATION? ? OR STRIPP???)
S9
                S2 AND S3 AND S6 AND S7
            1
S10
           63
                S2 AND S3 AND S6:S7
S11
           0
                S10 AND S8
           54
S12
                S10 NOT AD=20030123:20060622/PR
S13
           36
                S12 AND IC=(G05B OR G06F)
S14
            0
                S2 AND S3 AND S6 AND S8
S15
            5
                S2 AND S3 AND (S5 OR S7) AND S8
            5
S16
                S15 NOT (S9 OR S13)
                S16 NOT AD=20030123:20060622/PR
S17
            5
       300686
S18
                CONTROL(2N)SIGNAL? ?
          559
S19
                S18(3N) (SIMPLE? ? OR SIMPLIFY??? OR SIMPLIFIE? ?)
S20
            1
```

S19 AND S2 AND S3